

BF7264Pro

BF7264 Proは 下記の高速度バス専用プロトコルアナライザとして3世代目となる新製品です。
お問い合わせは立野電脳(株) sales@dsp-tdi.com

Acute BusFinder Protocol Analyzer & Logic Analyzer



270 x 175 x 55 (mm³)

- PC-based, 64 channels
- USB 3.0 interface, 12V power adaptor
- 32Gb total memory

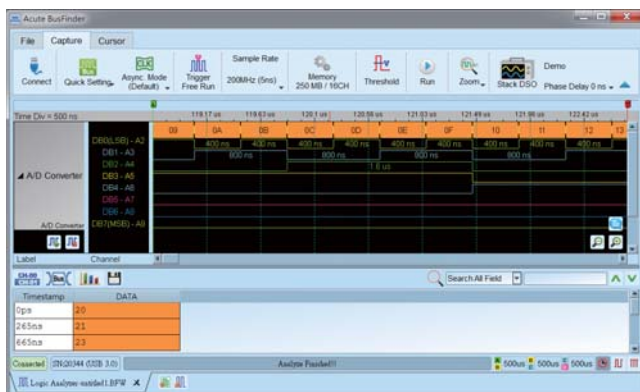
Protocol Analyzer: eMMC 5.1, MIPI D-PHY 1.2, NAND Flash, SD 3.0 (SDIO 3.0), SD 4.1 (UHS-II), SGMII, UFS2.1

- Real-time data display, post-capture waveforms
- Trigger for commands or data
- Different active probes for different protocols for easier connections
- Filter data to save more commands
- Hide data for easy reading
- Search data for quick finding
- Statistics for commands and data
- Two voltage detects to find design flaws from voltage drop
- Use PC hard disk drive (SSD) for long time recording
- Protocol monitor like dash camera for long time surveillance (months)

Logic Analyzer / LVDS: eMMC 5.1, NAND Flash, SD 3.0 (SDIO 3.0), Serial Flash, SPI

- 4GHz timing analysis
- 8-state flow chart bus triggers
- Protocol decodes with waveforms
- Stacks with a DSO to form as an MSO

Software Window



System Requirements

- USB 3.0 port
- Win 7, Win 8, Win 10, Win11
- PC RAM 16GB (recommended) or 8GB at least



立野電脳(株)はAcute Technologyの国内正規代理店です。
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Acute

PC-based T&M Instruments

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URL <https://www.dsp-tdi.com/>



BusFinder 7264Pro

Device : 800g
Accessories : 1216g

SGMII Option

weight : 410g



Device *1



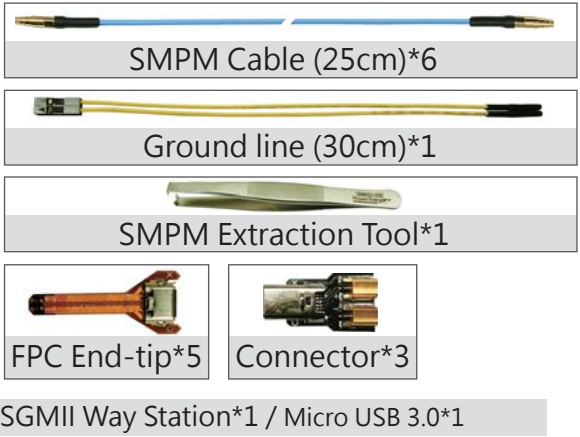
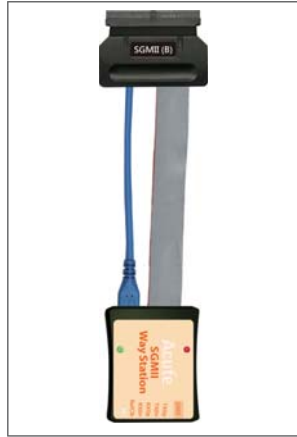
BNC to MCX cable *1



Power Adapter*1

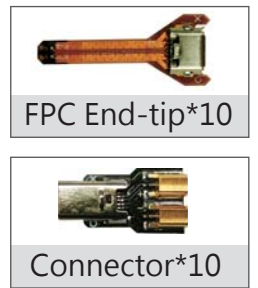
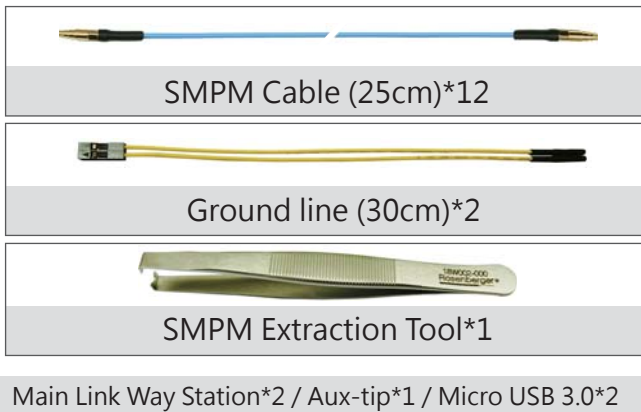


USB3.0 cable (1.8M) *1



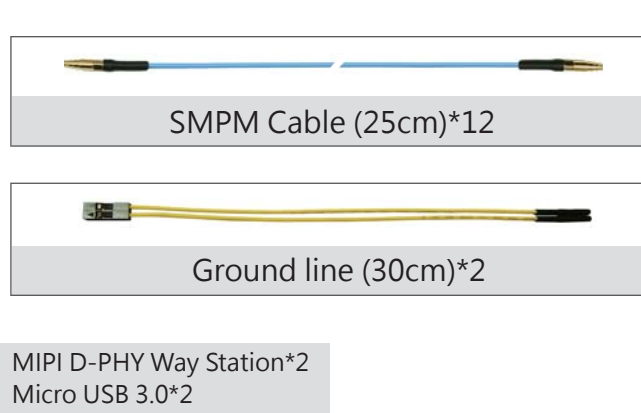
eDP1.4a Option

weight : 520g



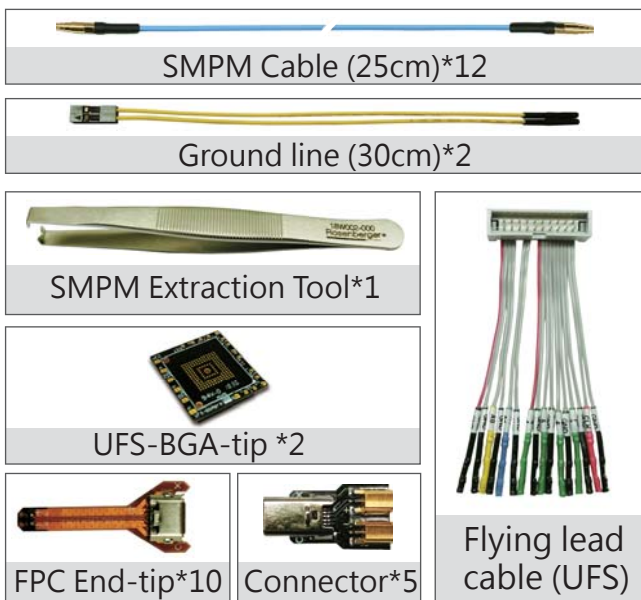
MIPI D-PHY Option

weight : 410g



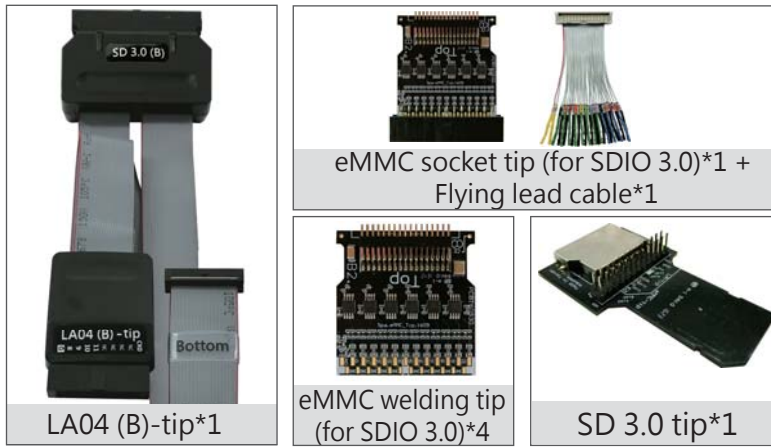
UFS 2.1 Option

weight : 550g



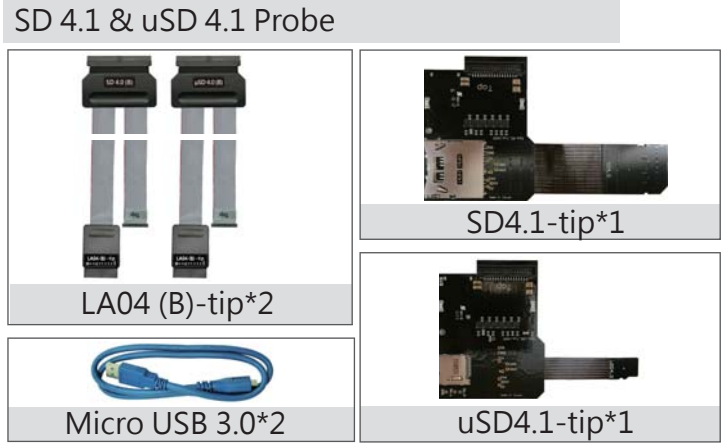
SD 3.0 / SDIO3.0 Option

weight : 160g



SD 4.1 Option

weight : 420g



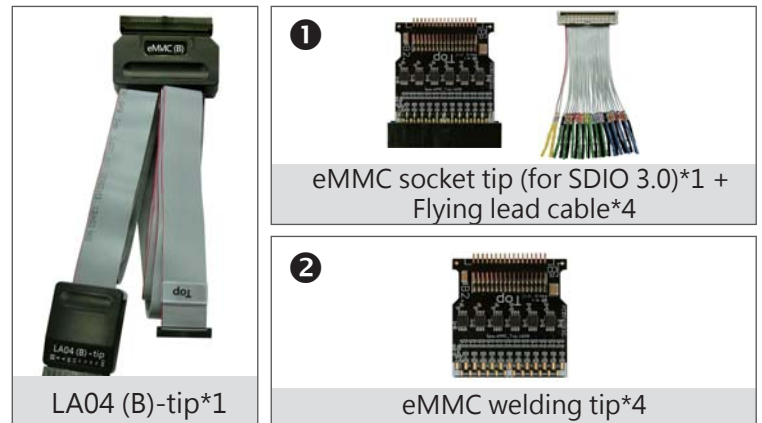
NAND Flash Option

weight : 450g



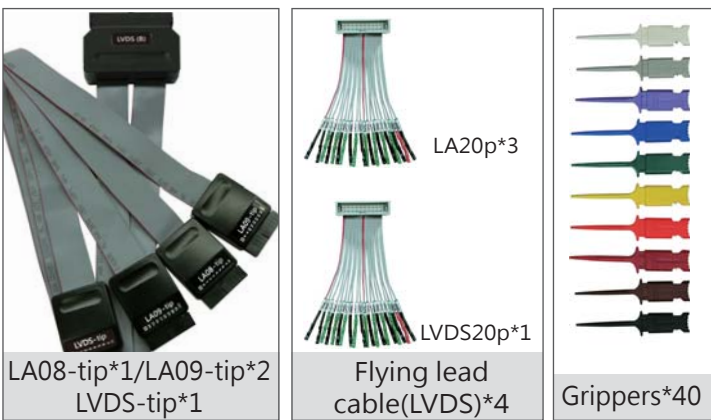
eMMC 5.1 Option

weight : 230g



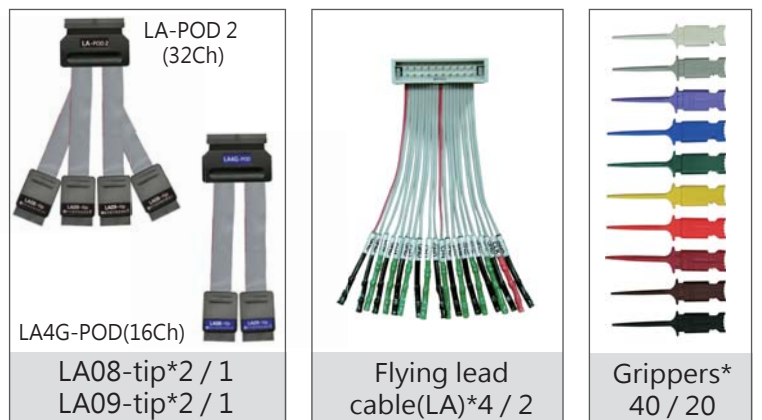
LVDS Option

weight : 450g



LA-POD 2 or LA4G-POD Option

weight : 450g



Tip specification

*SE: Single Ended, Diff.: Differential Pair

Model	LVDS	LA08/09	LA04 (B)	NAND, UFS	eMMC, SD 3.0	SD 4.1, uSD 4.1	End-tip
Number of Channels	8-Diff.	8 / 8+1 (Data+CLK)	4 (Data)	4+2 (Data+Analog)	12+2 (Data+Analog)	6-SE / 3-Diff. / 2 (SD3.0 / SD4.1 / Analog)	1-Diff.
Threshold of Data	Range	---	±15V	-0.5V ~ +4.8V	0V ~ +3.3V		---
	Resolution	---	10mV	21mV		---	
	Accuracy	---	±100mV + 5% *Vth				---
Input Voltage of Data	Maximum (Non-destructive)	-0.5V ~ +4.6V DC+AC peak	±40V DC+ AC peak	±15V DC+AC peak	-0.5V ~ +5V DC+AC peak		±10V
	Operation	0V ~ 3.3V	±15V	-1V ~ 8V	0V ~ 3.3V		0 ~ 5V
	Sensitivity	~100mV	~300mV		~150mV		~200mVpp.
Impedance of Data	75K Ω 3pF	~ 55KΩ <2pF to 1Vdc	1MΩ 5pF		500kΩ 2pF		¹ 1kΩ (500+1pF) ² 250Ω
Input Voltage of Analog	Maximum (Non-destructive)	---		-0.5V ~ +8V DC+AC peak			---
	Operation	---		0V ~ 4V			---
	Resolution	---		~1mV			---
	Sampling Rate	---		1M			---
Impedance of analog	---		1MΩ 100pF			---	

BusFinder

Model	BF7264Pro	
Power	Power Source	12V Power adapter
	Static Power Consumption	18W
	Max Power Consumption	45W
Hardware Interface	USB 3.0	
Timing Analysis (Asynchronous, Max. Sample Rate)	4GHz*	
State Clock Rate (Synchronous, External Clock)	400MHz	
Storage	Conventional Timing, Transitional Timing	
Channels (Data / Clock)	64/4	
Total Sample Memory	32 Gb	
Timing vs. Channels vs. Memory	Timing Analysis	Available channels (Conventional / Transitional Timing) - Memory per channel
	4GHz*	16/16 - 2Gb
	2.4 / 2GHz	32/32 - 1Gb
	1GHz/ 500, 250, 200MHz	64/64 - 500Mb
Trigger	Resolution	250 ps*
	Channels	64
	States	8
	Events	8
	Pre / Post / Delay	Yes
	Pass Counter	Yes (1 ~ 1000000 times)
	Types	Channel, Pattern, Single / Multi Level, Width, Time-out, External
	Bus (by option)	eMMC 5.1, NAND Flash, SD 3.0 (SDIO 3.0), Serial Flash (SPI NAND), SPI
Input Voltage	Maximum	See Tip specification
	Sensitivity	See Tip specification
Impedance	See Tip specification	
Temperature	Operating / Storage	5°C~45°C (41°F~113°F)/-10°C~65°C (14°F~149°F)
Channel to channel skew	< 250 ps*	
I/O port	Trig-In	TTL 3.3V level (Rising / Falling)
	Trigger pulse approval	> 8ns
	Trig-Out	TTL 3.3V
	Ref. Clock Input	10MHz, Vpp=3.3 to 5V
	Ref. Clock Output	10MHz, TTL3.3V
	Connector type	MCX jack/female
	Protocol Option	eDP 1.4a
eMMC 5.1		Flying lead cable / Gripper
MIPI D-PHY 1.2		SMPM Cable / End-tip
NAND Flash		Flying lead cable / Gripper
SD 3.0 / SDIO 3.0		SD 3.0 extender card / SDIO 3.0 extender card & Flying lead cable
SD 4.1		SD 4.0 extender card (covers SD 3.0)
SGMII		SMPM Cable / End-tip
UFS 2.1		Con Fixture / SMPM Cable / End-tip
LA-POD 2 or LA4G-POD		Flying lead cable / Gripper
LVDS		Flying lead cable / Gripper
Software Features	Zoom In / Out	Yes
	Languages	English / Traditional Chinese / Simplified Chinese
	Waveform Height	Adjustable
	Tools and quick settings	Zoom/ Report Window, Quick Cursor-positioning/ Protocol Decode Setup, Import Label(s)
	Trigger / Auxiliary cursors	1/25
	Protocol Decode	eMMC 5.1, NAND Flash, SD 3.0, Serial Flash (SPI NAND), SPI
Dimension	L x W x H	270 x 175 x 55 (mm ³)

※ For BF7264Pro, the maximum delivery Gripper number is 80 Gridders per mainframe unit.

* Optional LA4G-POD needed.