KAYA INSTRUMENTS
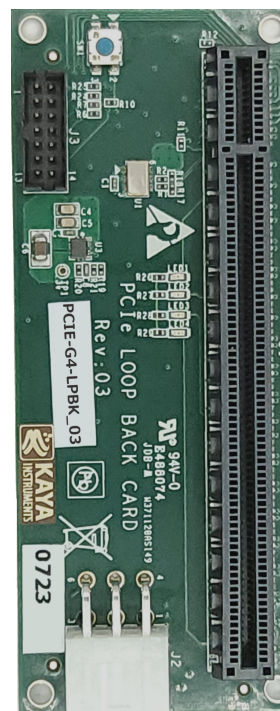
PCIe Gen4 x16 lanes Loopback Test Board

Overview

The **PCIe Gen 4 x16 lanes loopback tester board** enables developers and assembly factories to test and characterize the **PCIe** board interfaces. The board features full differential loopbacks on all the **PCIe** signals, JTAG interface. It also provides a 100MHz reference clock as per PCIe specification. The PCIe loopback card designed for signal integrity to support Gen4 speed of differential lanes.

Features

- PCIe FPGA board testing during development
- PCIe board electrical validation
- PCIe board testing at assembly facilities

Specifications

- Provides loopbacks on differential signals
- JTAG interface
- Reset button
- 100MHz reference clock

以下の型番でお問い合わせください。

- KY-PCIE-G4-LPBK

About PCIe

PCI Express (Peripheral Component Interconnect Express) is a high –speed serial computer expansion bus standard, including high maximum system bus throughput, low I/O pin count and small physical footprint, improved performance scaling for bus devices, detailed error detection and reporting mechanism (Advanced Error Reporting, AER), native hot-swap functionality and I/O virtualization. The primary purpose of an expansion card is to provide or expand on features not offered by the motherboard.
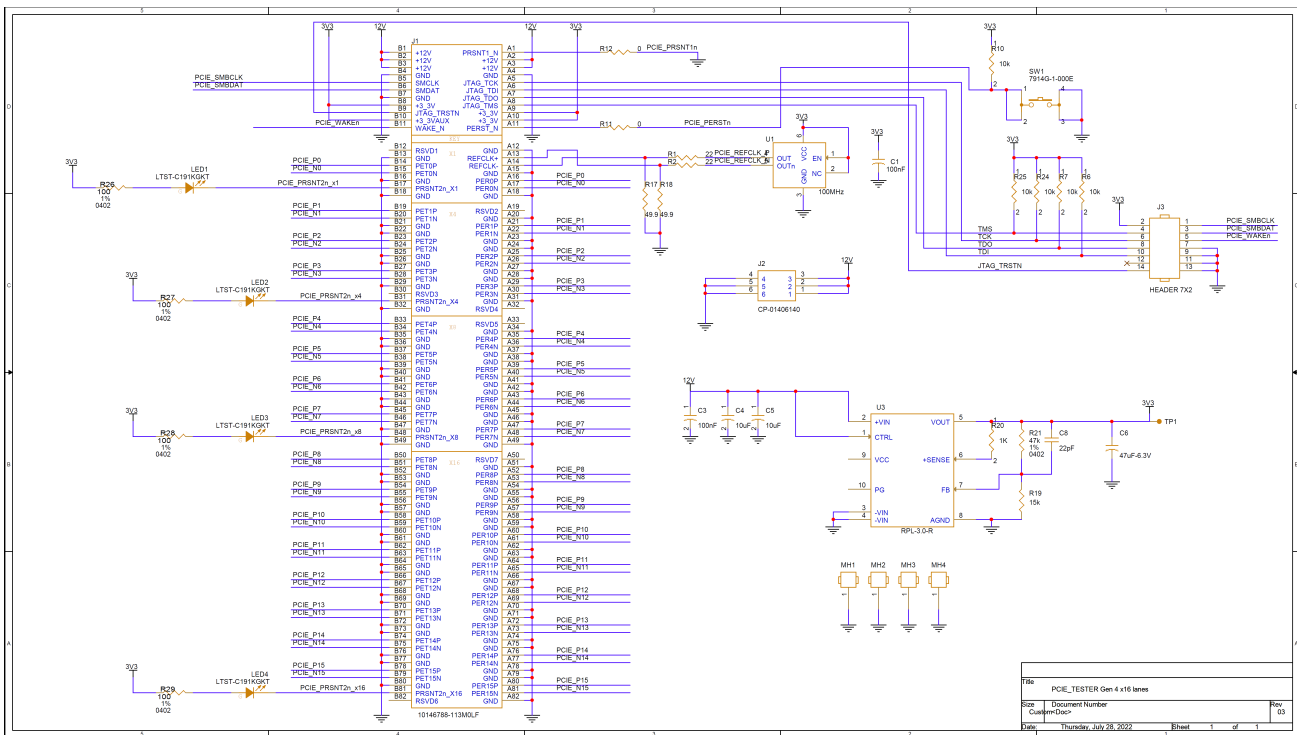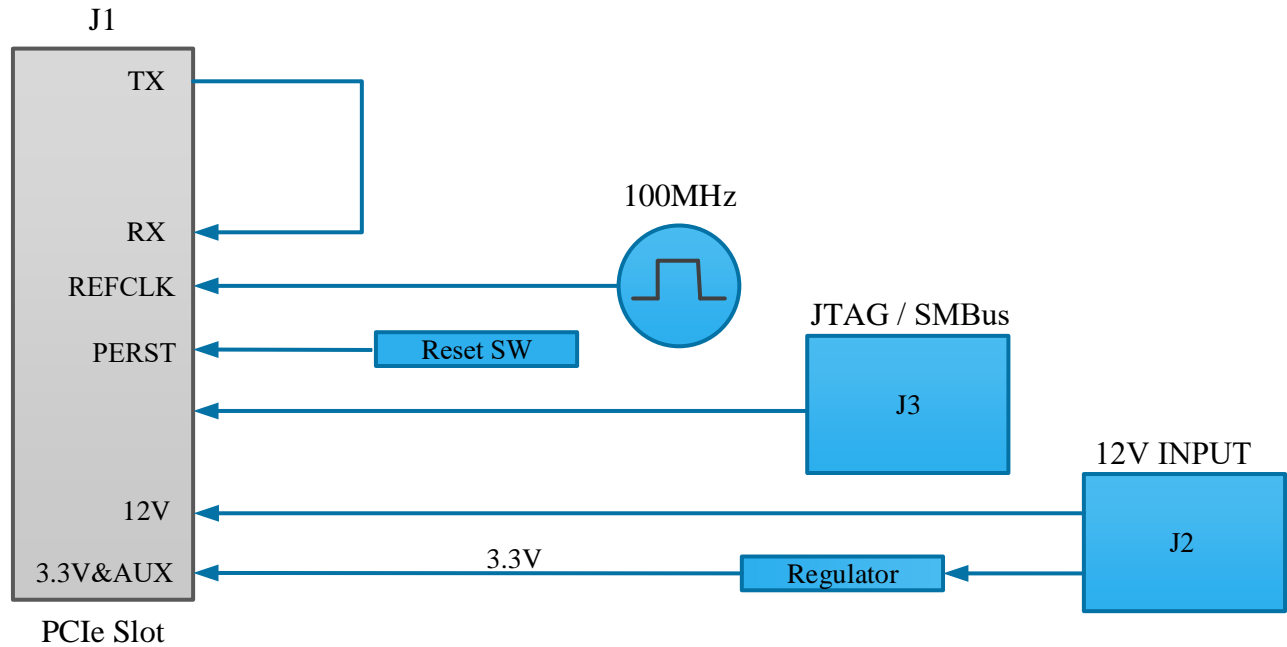
PCI EXPRESS®

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Block Diagram

J1

PCIe Slot

- TX
- RX
- REFCLK
- PERST
- 12V
- 3.3V&AUX

100MHz

Reset SW

JTAG / SMBus — J3

12V INPUT — J2

Regulator — 3.3V

PCIE_TESTER Gen 4 x16 lanes

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