



型番 KY-FGP-200 CoaXPress CXP-6 2レーン フレームグラバ
CoaXPress 規格適合品としてJIIAのサイトで紹介されています。(2015~)

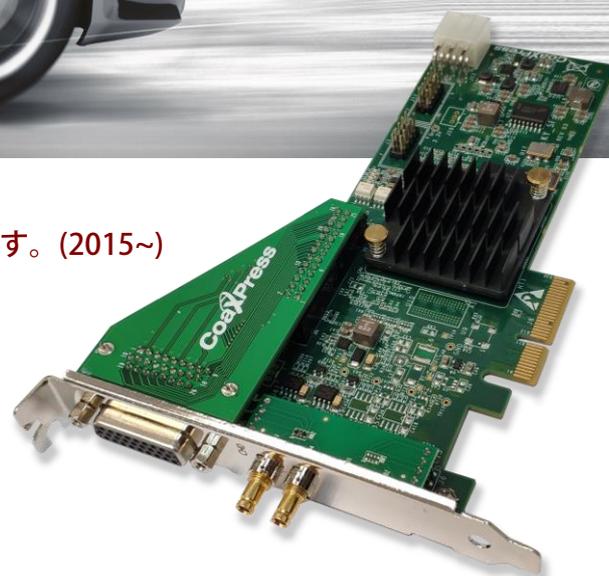
<http://jiia.org/host010/>

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国内在庫品

Key Features:

- 1 and 2 CoaXPress links support
- Multi-stream support
- Camera controls and triggers
- Per-link LED indication on card bracket
- Flexible machine I/O:
 - 4 TTL configurable I/Os
 - 4 LVCMOS configurable I/Os
 - 2 LVDS inputs
 - 2 LVDS outputs
 - 4 opto-isolated outputs
 - 4 opto-isolated inputs
 - 4 quadrature rotary encoders
 - Integrated strobe controller
- CoaXPress V1.1 compliant
- Power over CoaXPress with 13W per link
- Multiple Camera synchronization
- Multiple Frame Grabbers synchronization
- DIN 1.0/2.3 connectors for CoaXPress links
- GUI interface
- CoaXPress drivers for loopback function
- Supporting Windows and Linux OS
- API for developing custom applications
- Plug-ins modules for Matlab, HALCON and Labview
- Gen<i>Cam compliant
- GenTL support
- 4 Gb image buffer
- PCIe Gen2 x4 Half-length Low profile PCIe card
- Full or Half-height bracket
- Data rates up to 6.25 Gbps per link
- Transfer Rate of up to 12.5 Gbps
- 0°C to 50°C operating environment temperature



Predator Frame Grabber with 2 channels

Innovative

Predator is a low-cost Frame Grabber supporting CoaXPress standard. The Predator is capable of receiving video streams from up to 2 CoaXPress links in single or dual modes. Each link supports standard CoaXPress bitrates up to 6.25 Gbps including PoCXP. This CoaXPress frame grabber is ideally suited for industrial, defense and aerospace Machine Vision Systems and applications.

Intelligent Design

The Predator can easily receive video streams on the CoaXPress links and transmit them to computer memory through the PCIe interface. This product also provides GPIO for machine control signals such as triggers, shaft encoders, exposure control and general I/O, which can be controlled aside the video stream acquisition.

The Predator uses standard DIN connectors as a CoaXPress interface to the camera and standard 100 mil headers for general purpose I/O. The frame grabber utilizes PCIe Gen2 x4 links for communication with Host PC for video uploading and configuration.

Datasheet | Predator™ 2 Channel – CoaXPress Frame Grabber



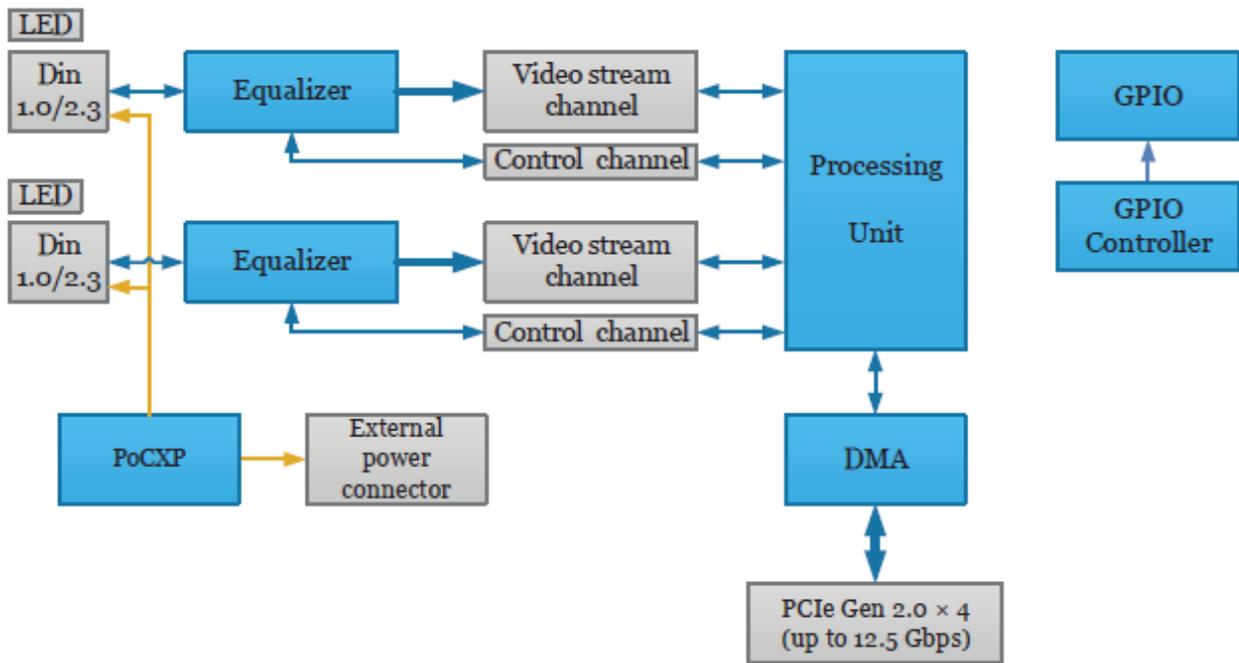
Product Name	Predator™ 2 Channel – CoaXPress Frame Grabber
Form Factor	PCI Express card
Format	Low profile, half length, 4-lane PCI Express card
Cooling method	Air cooling, passive heatsink
Mounting	For insertion in a low profile, 4-lane or higher, PCI Express card slot
Connectors	Ports 0 through 1 on bracket 2x DIN 1.0/2.3 female connectors CoaXPress host interface Internal I/O connector 1 on PCB 26-pin 2-row 0.1" pitch pin header with shrouding for I/O lines Internal I/O connector 2 on PCB 26-pin 2-row 0.1" pitch pin header with shrouding for I/O lines Auxiliary power input (PoCXP) on PCB 6-pin PEG power socket 12 VDC power input for PoCXP camera(s)
Dimensions	L 167.65 mm x H 68.9 mm L 6.6 in x H 2.71 in
Weight	130gr
Host bus	
Standard	PCI Express 2.0
Link width	4 lanes 1 or 2 lanes with reduced performance
Link speed	5.0 GT/s (PCIe 2.0)
Maximum payload size	512 bytes
DMA	<ul style="list-style-type: none"> • 32- and 64-bit • Scatter gather support • Physical address support (GPU transfers)
Peak delivery bandwidth	2,000 MB/s
Effective (sustained), delivery bandwidth	1,700 MB/s (Host PC motherboard dependent)
Power consumption	Typ. 7 W, excluding camera and I/O power output

Camera / video inputs	
Interface standard(s)	CoaXPress 1.0 and 1.1
Connectors	2x DIN1.0/2.3 CXP-6
Status LEDs	1 CoaXPress Host connection status per connector 4 System status LEDs
Number of cameras	Up to 2
Number of links, per single camera	Up to 2
Synchronization between cameras	Yes
Line-scan cameras supported,	Yes
Maximum aggregated camera data transfer rate	12.5 Gbit/s
Supported CXP down-connection speeds	<ul style="list-style-type: none"> • 1.25 GT/s (CXP-1) • 2.5 GT/s (CXP-2) • 3.125 GT/s (CXP-3) • 5 GT/s (CXP-5) • 6.25 GT/s (CXP-6)
Supported CLHS speeds	
Number of data streams (per camera)	1 data stream per camera
Maximum stream packet size	8,192 bytes
PoCXP (Power over CoaXPress)	<ul style="list-style-type: none"> • PoCXP Safe Power:; • 13 W of 24V DC regulated power per CoaXPress connector • PoCXP Device detection and automatic power-on • Overload and short-circuit protections • On-board 12V to 24V DC/DC converter • A +12V power source must be connected to the auxiliary power input connector
Camera types	<ul style="list-style-type: none"> • Area-scan cameras: Gray-scale and color (RGB and Bayer CFA) Single-tap (1X-1Y) progressive-scan • Line-scan cameras: Gray-scale and color RGB
Camera pixel formats supported	<p>Raw, Monochrome, Bayer, RGB, YUV, YCbCr and RGBA (PFNC names):</p> <ul style="list-style-type: none"> • Raw • Mono8, Mono10, Mono12, Mono14, Mono16 • BayerXX8, BayerXX10, BayerXX12, BayerXX14, BayerXX16 where XX = GR, RG, GB, or BG • RGB8, RGB10, RGB12, RGB14, RGB16 • RGBA8, RGBA10, RGBA12, RGBA14, RGBA16 • YUV411_8, YUV411_10, YUV411_12, YUV411_14, YUV411_16 • YUV422_8, YUV422_10, YUV422_12, YUV422_14, YUV422_16 • YUV444_8, YUV444_10, YUV444_12, YUV444_14, YUV444_16 • YCbCr601_411_8, YCbCr601_411_10, YCbCr601_411_12, YCbCr601_411_14, YCbCr601_411_16 • YCbCr601_422_8, YCbCr601_422_10, YCbCr601_422_12, YCbCr601_422_14, YCbCr601_422_16 • YCbCr601_444_8, YCbCr601_444_10, YCbCr601_444_12, YCbCr601_444_14, YCbCr601_444_16
Area-scan camera control	
Trigger	<p>Precise control of asynchronous reset cameras, with exposure control.</p> <p>Support of camera exposure/readout overlap.</p> <p>Support of triggering from encoder or timer</p> <p>Support of external hardware trigger, with optional delay, filtering and trigger decimation.</p>
Strobe	<p>Accurate control of the strobe position for strobe light sources.</p> <p>Support of early and late strobe pulses.</p>

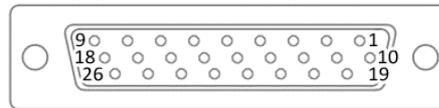
Line-scan camera control	
Scan/page trigger	Precise control of start-of-scan and end-of-scan triggers. Support of external hardware trigger, with optional delay and filtering. Support of triggering from encoder Support of infinite acquisition, without missing lines.
Line trigger	Support for quadrature motion encoders, with programmable filters, selection of acquisition direction and backward motion compensation.
Line strobe	Accurate control of the strobe position for strobe light sources.
On-board processing	
On-board memory	2 Gb
Decimation	Line skip
Additional features	Unpacking of 10-/12-/14-bit to 16-bit with justification to LSB
Frame Timestamp	64bit with 8ns precision
Data stream statistics	Measurement of: <ul style="list-style-type: none"> • Frame/Line rate • CRC Errors • Dropped frames • Received packets • Test packets
Event signaling and counting	The application software can be notified of the occurrence of various events: <ul style="list-style-type: none"> • Newly acquired buffers • Camera and Illumination control events • I/O events • Timer events • Encoder events
General Purpose Inputs and Outputs	
Number of lines	20 I/O lines: <ul style="list-style-type: none"> 2 differential inputs 2 differential outputs 4 singled-ended TTL inputs/outputs 4 singled-ended LVCTTL inputs/outputs 4 opto-isolated inputs 4 opto-isolated outputs
Usage	Any System I/O input lines can be connected to any I/O line Any I/O line can be used to decode A/B and Z signals of a motion encoder Any I/O line can generate any trigger event Any I/O line can trigger a timer
Electrical specifications	Differential lines - LVDS compatible TTL lines - 5V TTL compliant LVTTTL lines - 3.3V LVTTTL compliant Isolated lines - opto isolated lines with voltage range up to 30V
Filter control	Glitch removal filter available on all System I/O input lines Configurable filter time constants: for DIN and TTLIO lines: 50 ns, 100 ns, 200 ns, 500 ns, 1 μ s for IIN lines: 500 ns, 1 μ s, 2 μ s, 5 μ s, 10 μ s
Polarity control	Yes
Encoders	8 quadrature encoders with A/B and Z inputs 32bit position counter Forward and backward counting Position trigger support Noise filtering
Timers	8 general purpose timers Configurable delay and duration 32bit accumulator

Event reporting	64 bit system timestamp event reporting Each I/O line can generate event on configurable edge Each Timer can generate event Each encoder can generate event
Frame grabber synchronization	
Synchronization	Precise area and linscan cameras synchronization across different frame grabbers
Software	
Host PC Operating System	Microsoft Windows 7/10 32- and 64-bit versions, Linux open source driver compatible with a wide range of distributions, tested and precompiled for Ubuntu 14.04 , RedHat 6.5 , CentOS 7 32- and 64-bit versions
Buffer management	Circular buffer support Accumulation of several frames/lines to single buffer to reduce CPU load DMA Buffer filling directly to system memory
GUI	Supported for Windows and Linux OS Multi-camera display and configuration Flexible buffer queuing Image/video recording
Debugging capabilities	Event logging Statistics counters
Gen<i>Cam	Support of Gen<i>Cam up to 2.4 Full camera and frame grabber parameters configuration
Environmental conditions	
Operating ambient air temperature	0°C to +50°C / +32°F to +122 °F
Operating ambient air humidity	10% to 90% RH non-condensing
Storage ambient air temperature	-20°C to +70°C / -4°F to +158°F
Storage ambient air humidity	10% to 90% RH non-condensing
Certifications	
Electromagnetic - EMC standards	The European Council EMC Directive 2004/108/EC The Unites States FCC rule 47 CFR 15
EMC - Emission	EN 55022:2010 Class B FCC 47 Part 15 Class B
EMC - Immunity	EN 55024:2010 Class B EN 61000-4-3 EN 61000-4-4 EN 61000-4-6
Flammability	PCB compliant with UL 94 V-0
RoHS	Compliant with the European Union Directive 2011/65/EU (ROHS2)
REACH	Compliant with the European Union Regulation No 1907/2006
WEEE	Must be disposed of separately from normal household waste and must be recycled according to local regulations
Ordering Information	
Optional accessories	GPIO Expansion bracket

Predator Frame Grabber HW Block Diagram



Predator Frame Grabber GPIO Pin-out



Pin	Signal Name	Function	Electrical	Description
1	LVDS Input 0p	LVDS input	LVDS	Positive signal of LVDS pair
2	LVDS Input 0n	LVDS input	LVDS	Positive signal of LVDS pair
3	LVDS Input 1p	LVDS input	LVDS	Negative signal of LVDS pair
4	LVDS Input 1n	LVDS input	LVDS	Negative signal of LVDS pair
5	LVDS Output 0p	LVDS output	LVDS	Positive signal of LVDS pair
6	LVDS Output 0n	LVDS output	LVDS	Positive signal of LVDS pair
7	LVDS Output 1p	LVDS output	LVDS	Negative signal of LVDS pair
8	LVDS Output 1n	LVDS output	LVDS	Negative signal of LVDS pair
9	Optocoupled Output 0	Opto-Isolated output	Up to 70V	Optically isolated outputs
10	Optocoupled Output 1	Opto-Isolated output	Up to 70V	Optically isolated outputs
11	Optocoupled Output 2	Opto-Isolated output	Up to 70V	Optically isolated outputs
12	Optocoupled Output 3	Opto-Isolated output	Up to 70V	Optically isolated outputs
13	Optocoupled Input 0	Opto-Isolated input	Up to 70V	Optically isolated inputs
14	Optocoupled Input 1	Opto-Isolated input	Up to 70V	Optically isolated inputs
15	Optocoupled Input 2	Opto-Isolated input	Up to 70V	Optically isolated inputs
16	Optocoupled Input 3	Opto-Isolated input	Up to 70V	Optically isolated inputs
17	Optocoupled GND	External GND		Ground signal for opto-isolated signals on this connector
18	GND	Board GND		Reference ground signal
19	TTL 0	GPIO	TTL (Open-drain)	General Purpose IO
20	TTL 1	GPIO	TTL (Open-drain)	General Purpose IO
21	TTL 2	GPIO	TTL (Open-drain)	General Purpose IO
22	TTL 3	GPIO	TTL (Open-drain)	General Purpose IO
23	LVTTTL 0	GPIO	LVTTTL	General Purpose IO
24	LVTTTL 1	GPIO	LVTTTL	General Purpose IO
25	LVTTTL 2	GPIO	LVTTTL	General Purpose IO
26	LVTTTL 3	GPIO	LVTTTL	General Purpose IO